

IN THE CLAIMS

Please amend the claims as follows:

1. (currently amended) An LCD array having a metal circuitry layer separated from a plurality of imaging surfaces by an insulating layer, comprising:

a plurality of vias between the metal layer and each of the imaging surfaces; and  
wherein each of the vias passes through the insulating layer.

2. (original) The LCD array of claim 1, wherein:

each of the vias is an electrical connection between the metal layer and the imaging surfaces.

3. (original) The LCD array of claim 1, wherein:

the imaging surfaces are mirrors.

4. (original) The LCD array of claim 1, wherein:

the quantity of vias is two per imaging surface.

5. (currently amended) The LCD array of claim 1, wherein:

the vias are uniformly distributed ~~[[on]]~~ along both an X-axis a first direction and a Y-axis  
second direction of the LCD array.

6. (currently amended) The LCD array of claim 1, wherein:

the vias underlying each imaging surface are equidistant from ~~an X-axis~~ a first axis of  
~~each of the respective imaging surfaces~~ surface.

7. (currently amended) The LCD array of ~~claim 1~~ claim 6, wherein:

the vias underlying each imaging surface are equidistant from a ~~Y-axis~~ second axis of  
~~each of the respective imaging surfaces~~ surface.

8. (currently amended) The LCD array of claim 1, wherein:

the vias are grouped near a center of each of the imaging surfaces; and  
the vias are disposed on at least one of an X axis and a Y axis of the imaging surfaces.

9. (currently amended) A method for positioning vias under mirrors of an LCD array, comprising:

placing ~~[[the]]~~ a plurality of vias under each of a plurality of mirrors such that the vias are evenly spaced from the center of ~~the mirrors~~ each respective mirror with respect to both an X axis and a Y axis of the respective mirror.

10. (currently amended) The method for positioning vias ~~on~~ under mirrors of ~~[[Fig. 9]]~~ an LCD array of claim 9, and further including:

grouping the vias near the center of ~~the mirrors~~ each respective mirror on at least one of the X axis and the Y axis of the respective mirror.

11. (currently amended) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces such that the vias are evenly distributed on the imaging surfaces and pass through the insulating layer.

12. (currently amended) The LCD array of claim 11, wherein:

the vias are symmetrically arrayed ~~[[on]]~~ about at least one of an X axis and a Y axis of the LCD array.

13. (currently amended) The LCD array of claim 11, wherein:

the quantity of vias associated with each ~~mirror~~ imaging surface is two; and

the vias are symmetrically arrayed ~~[[on]]~~ about at least one of an X axis and a Y axis of the LCD array; and

the vias are grouped together near the center of the ~~mirror~~ imaging surface on at least one of the X axis and the Y axis of the imaging surface.

14. (original) The LCD array of claim 11, wherein:

each of the vias is an electrical conductor between the metal layer and one of the imaging surfaces.

15. (currently amended) The LCD array of claim 11, wherein:

the vias are symmetrically arrayed ~~along at least one~~ about a first axis of the LCD array.

16. (currently amended) The LCD array of claim 15, wherein:

the vias are symmetrically arrayed ~~along~~ about a second axis of the LCD array.

17. (currently amended) The LCD array of claim 15, wherein:

the vias of each imaging surface are grouped together about a center of the mirror each respective imaging surface; and

in a second axis of the LCD array the vias of each imaging surface are aligned along an axis of each respective imaging surface, the axes of the imaging surfaces being parallel to one another.

18. (currently amended) The LCD array of claim 11, wherein:

two of the vias are positioned near the center of each of the ~~mirrors~~ imaging surfaces along a direction parallel to at least one axis of the LCD array.

19. (currently amended) The LCD array of claim 11, wherein:

two of the vias are symmetrically arrayed ~~along~~ about an axis parallel to at least one of ~~[[the]]~~ an X axis and ~~[[the]]~~ a Y axis of the LCD array on each of the ~~mirrors~~ imaging surfaces.

20. (currently amended) The LCD array of claim 11, wherein:

two of the vias are ~~symmetrically arrayed~~ disposed along an axis parallel to a Y axis of the LCD array on each of the imaging surfaces.

21. (new) An LCD array having a metal circuitry layer separated from a plurality of imaging surfaces by an insulating layer, comprising:

a plurality of vias between the metal layer and each of the imaging surfaces; and

wherein the vias are uniformly distributed on both an X axis and a Y axis of the LCD array.

22. (new) An LCD array having a metal circuitry layer separated from a plurality of imaging surfaces by an insulating layer, comprising:

a plurality of vias between the metal layer and each of the imaging surfaces; and

wherein the vias are equidistant from an X axis of each of the imaging surfaces.

23. (new) The LCD array of claim 22, wherein:

the vias are equidistant from a Y axis of each of the imaging surfaces.

24. (new) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces such

that the vias are evenly distributed on the imaging surfaces; and

wherein the quantity of vias associated with each imaging surface is two;

the vias are symmetrically arrayed on at least one of an X axis and a Y axis; and

the vias are grouped together near the center of the imaging surface on at least one of the X axis and the Y axis.

25. (new) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces

such that the vias are evenly distributed on the imaging surfaces; and

wherein the vias are symmetrically arrayed along a first axis of the LCD array; and

the vias are symmetrically arrayed along a second axis of the LCD array.

26. (new) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces such that the vias are evenly distributed on the imaging surfaces; and wherein the vias are symmetrically arrayed along a first axis of the LCD array; the vias of each imaging surface are grouped together about a center of each respective imaging surface; and the vias of each imaging surface are aligned along an axis of each respective imaging surface, the axes of the imaging surfaces being parallel to one another.

27. (new) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces such that the vias are evenly distributed on the imaging surfaces; and wherein two of the vias are positioned near the center of each of the imaging surfaces along at least one axis of the LCD array.

28. (new) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces such that the vias are evenly distributed on the imaging surfaces; and wherein two of the vias are symmetrically arrayed along at least one of an X axis and a Y axis of the LCD array on each of the imaging surfaces.

29. (new) In an LCD array having a plurality of imaging surfaces separated from a metal layer by an insulating layer, an improvement comprising:

providing a plurality of vias between the metal layer and each of the imaging surfaces such that the vias are evenly distributed on the imaging surfaces; and wherein two of the vias are symmetrically arrayed along a Y axis of the LCD array.

30. (new) An LCD array having a metal circuitry layer separated from a plurality of imaging surfaces by an insulating layer, comprising:

a plurality of vias between the metal layer and each of the imaging surfaces; and  
wherein each of the vias pass through the first insulating layer under the imaging surfaces.

31. (new) An LCD array having a metal circuitry layer separated from a plurality of imaging surfaces by an insulating layer, comprising:

a plurality of vias between the metal layer and each of the imaging surfaces; and  
wherein the vias establish parallel electrical connections to each imaging surface.

IN THE DRAWINGS

Applicant requests permission to amend FIG. 3 as shown in the attached replacement sheet.

IN THE ABSTRACT

Please amend the abstract as follows:

ABSTRACT ABSTRACT

A reflective LCD array (10) configured to minimize distortion in mirrors (14) thereof produced by attachment of a plurality of vias (16) to the mirrors (14). The vias (16) electrically connect the mirrors (14) to a metal layer (12) having circuitry thereon associated with the mirrors (14). The vias (16) are positioned equidistant about a center (30) of the mirrors (14) such that spacing of the vias (16) is generally equidistant from an X axis (26) and Y axis (28) of the reflective LCD array 10. The vias (16) are positioned such that such symmetry is retained even where circuitry associated with adjacent mirrors (14) is a reflected image copy of the reference mirror (14). Where even spacing about either the X axis (26) or the Y axis (28) is not practical, the vias (16) are grouped near the center (30) of the mirror (14) along that axis such that the effect is that of there being only a single via (16) along such axis.